P	D8IS767 Page No 1
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	P.E.S. College of Engineering, Mandya - 571 401
and the	(An Autonomous Institution affiliated to VTU, Belgaum)
	Seventh Semester, B.E Information Science and Engineering
	Semester End Examination; Dec 2014 ARM Based System Design
1	Time: 3 hrs Max. Marks: 100
	<i>Note:</i> Answer any <i>FIVE</i> full questions, selecting at least <i>TWO</i> full questions from each part
	PART - A
	Briefly explain the AMBA bus Protocol.
b.	Discuss the concept of Pipeline in ARM Processor.
с.	Write a short note on ARM processor families.
	Explain the different data processing instructions in detail with suitable example.
	With an example explain the concept of saturated arithmetic.
	Bring out the salient features of profiling and cycle counting in ARM simulator.
b.	With an example of your choice explain the concept of unrolled counted loops.
c.	With a suitable example explain how to handle unaligned data in ARM architecture.
4 a.	Explain the three stages of the logarithm calculation in detail.
b.	Explain in detail how to calculate unsigned 32\32-bit divide by Newton-Raphson method.
	PART - B
5 a.	With a neat diagram, explain the non nested interrupt handler.
b.	Briefly explain exception priorities.
c.	Write a short note on vector table.
ба.	Explain the concept of measuring Cache efficiency.
b.	Discuss the Cache line replacement policies and explain any one briefly with a suitable example.
c.	Explain the cleaning process for the D-Cache using the Test clean command.
7 a.	How do we set the region cache and write buffer attributes? Briefly explain.
b.	Explain region assignment using a memory Map with a suitable example.
c.	Write short notes on memory organization in a virtual memory system.
8 a.	With a help of neat circuit diagram explain the following:
	(i) Single-step page table walk
	(ii) Two-step page table walk.
b.	Briefly explain the Fast context switch Extension (FCSE) with a suitable example.