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	U.S.N	
	P.E.S. College of Engineering, Mandya - 571 401 (An Autonomous Institution affiliated to VTU, Belgaum) First Semester, M. Tech – VLSI Design and Embedded System (MECE) Semester End Examination; Jan - 2017 SOC Design Time: 3 hrs Max. Marks: 100	
	<i>Note:</i> Answer <b>FIVE</b> full questions, selecting <b>ONE</b> full question from each unit.	
	UNIT - I	
1 a	. Define the SOC. Explain various factors that are driving the industry to develop SOC.	0
b	. What are important specifications of SOC design process? Explain types of SIC specification.	2
с	. Outline the SOC design flow procedure.	(
2 a	. Explain Moore's law and its different interpretations.	(
b	. Compare SOC, SIP and SOB on different characteristics.	2
c	. What are the characteristics of a good IP? Explain.	(
	UNIT - II	
3 a	. Write at least six major differences between RISC and CISC processers.	(
b	. Describe Van-Newman and Harward architecture.	2
С	. Compare micro controller DSP and their selection criteria.	(
4 a	. What is cache coherency? Explain MESI protocol for cache coherency.	:
b	. Explain working of NAND Flush memory with a neat diagram.	0
c	. Describe basic DRAM with its architecture and also its variations to enhance the speed with	
	necessary timing diagram.	
	UNIT - III	
5 a	. Why hardware accelerators are needed in SOC? Mention their typical applications.	
b	. What are the types of data transfer modes? Explain each type with advantages and draw backs.	1
6 a	. What is network on chip? Explain direct, indirect and Hybrid topologies with a neat diagram.	1
b	. Explain warm hole switching.	
c	. Describe bus architecture and its limitations.	
	UNIT - IV	
7 a	. What is the resolution of 13 bit A/D converter? If the converter has a full scale output of IOV.	
	What is the size of each step? What will be the actual maximum output voltage if this	
	converter has to be consistent with its resolution?	
b	. Describe the main advantages of instrumentation amplifier over single differential amplifier.	

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c.	An RF signal has spectral band centered around $W_{RR}$ rad/sec. The local oscillator frequency is			
	$W_{no} < W_{RF.}$ Give the block diagram of Hartley image rejection receiver. Show by analysis.	8		
	How the image band is rejected?			
8 a.	What is the need for power management circuits in SOC? What are the sources of power	8		
	dissipation?	0		
b.	Explain the operation of RF transmitter and receiver circuits with relevant diagrams in detail.	8		
c.	Write short notes on amplifier need in SOC design.	4		
UNIT - V				
9 a.	With neat flow chart, Explain high level verifications for SOC devices.	10		
b.	Discuss the issues in Hardware software code sign.	10		
10.	Write short notes on the following :			
	i) ESL design flow			
	ii) USB controller	20		
	iii) Hard and soft IP			
	iv) Packaging problem in SOC.			

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