



P.E.S. College of Engineering, Mandya - 571 401

(An Autonomous Institution affiliated to VTU, Belagavi)

Fourth Semester, B.E. - Electronics and Communication Engineering

Semester End Examination; May/June - 2018

Power Electronics

Time: 3 hrs

Max. Marks: 100

Note: Answer **FIVE** full questions, selecting **ONE** full question from each unit.

UNIT - I

- 1 a. With a neat sketch, explain the control characteristics of power switching devices. 8
- b. With the help of a neat block diagram, explain generalized power converter system. 6
- c. List the advantages and disadvantages of MOSFET over BJT. 6
- 2 a. With the help of a neat transient model and waveforms, explain the transient model of BJT. 8
- b. Explain the need for base drive control with necessary equations, explain Turn on control. 6
- c. The Bipolar transistor shown in the Fig. Q2(c) is specified to have β in the range of 8 to 40. The load resistance $R_c = 11 \Omega$. The DC supply voltage $V_{CC} = 200 \text{ V}$ and the input voltage to the base circuit is $V_B = 10 \text{ V}$. If $V_{CE(Sat)} = 1 \text{ V}$ and $V_{BE(Sat)} = 1.5 \text{ V}$. Find;
 - i) The value of R_B that results in saturation with an overdrive factor of 5
 - ii) The forced β_f
 - iii) The power loss P_T in the Transistor

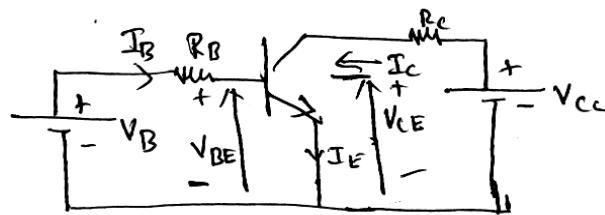


Figure Q2(c)

UNIT - II

- 3 a. With the help of a two transistor model for a thyristor, explain Regenerative action. 8
- b. Explain the different way of turning ON a thyristor. 10
- c. Differentiate between Phase-Control thyristor and Fast-Switching thyristor. 2
- 4 a. With the help of a neat diagram and waveforms, explain turn off characteristics of thyristors for a line commutated thyristor. 8
- b. Ten Thyristors are used in a string to withstand a DC voltage of $V_s = 15 \text{ kV}$. The maximum leakage current and recovery charge differences of thyristors are 10 mA and $150 \mu\text{C}$ respectively. Each thyristor has a voltage sharing resistance of $R = 56 \text{ k}\Omega$ and capacitance of $G = 0.5 \mu\text{F}$. Determine; 8
 - i) Maximum steady-state voltage sharing $V_{DS(Max)}$
 - ii) The steady state voltage derating factor
 - iii) The maximum transient voltage sharing $V_{DT(max)}$
 - iv) The transient voltage derating factor
- c. Explain UJT triggering circuit for full wave rectifier. 4

