



P.E.S. College of Engineering, Mandya - 571 401

(An Autonomous Institution affiliated to VTU, Belagavi)

Fourth Semester, B.E. - Electrical and Electronics Engineering

Semester End Examination; May / June - 2019

Microcontrollers

Time: 3 hrs

Max. Marks: 100

Note: Answer FIVE full questions, selecting ONE full question from each unit.

UNIT - I

- | | | |
|------|---|---|
| 1 a. | List out any four differences between Microprocessor and Microcontroller. | 4 |
| b. | Draw the functional block diagram of 8051 μ c and explain various 8-bit and 16-bit registers only. | 8 |
| c. | Explain and analyze the difference between two architectures based on instruction sets. | 8 |
| 2 a. | Draw the functional block diagram of 8051 μ c and explain the architecture excluding various registers. | 8 |
| b. | List and analyze the differences between the Von-Neumann and Harvard architecture based on memory. | 6 |
| c. | What is the size of internal ROM and RAM? Explain the internal RAM organization with relevant diagram. | 6 |

UNIT - II

- | | | |
|------|---|---|
| 3 a. | What is the need of stack? When μ c is powered on, SP is initialized to what value? Explain the stack operations with an example. | 8 |
| b. | What is the need of addressing modes? Mention the various addressing modes used in 8051 μ c. Explain any three addressing modes with an example. | 8 |
| c. | Explain and analyze the following instructions with an example :
(i) MOVX (ii) MOVC (iii) SWAP (iv) XCH | 4 |
| 4 a. | Explain and analyze the various byte level logical AND operation. | 6 |
| b. | Identify and explain the various addressing modes used on the following instructions :
(i) MOVC A, @A+PC (ii) MOVX A, @A+DPTR
(iii) MOV A, #25H (iv) MOV A, @R _i
(v) MOV R ₂ , 40H (vi) MOVX A, @R _i | 6 |
| c. | Identify the syntax in the following instructions and write the correct instructions with valid reason :
(i) MOV A, @R ₇ (ii) MOV #70H, R ₃ (iii) XCH R ₁ , R ₇ (iv) MOV DPTR, A | 8 |

UNIT - III

- | | | |
|------|---|---|
| 5 a. | Explain and analyze the following JUMP instructions with an example with respect to the distance :
(i) SJMP (ii) AJMP (iii) LJMP (iv) JMP @A+DPTR | 8 |
| b. | If ten bytes of data are stored from RAM locations 45H to 54H add 02 to each of them and save the result in RAM locations 79H down to 70H. | 8 |

