

- 8 a. Discuss the energy dissipation in Transistor channel using an RC model. 10
- b. Explain with diagram, Adiabatic dynamic logic in CMOS inverter. 10

UNIT - V

- 9 a. Discuss the sources of software power dissipation. 10
- b. Describe the software power estimation at gate architectural level and bus switching activity. 10
- 10 a. Explain the software power optimization using algorithm Transformation. 10
- b. Discuss power management for software optimization. 10

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