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P.E.S. College of Engineering, Mandya - 571 401

(An Autonomous Institution affiliated to VTU, Belagavi)
Fifth Semester, B.E. - Mechanical Engineering

Semester End Examination; Dec. - 2019 Mechatronics and Microprocessor

Time: 3 hrs Max. Marks: 100

Note: Answer *FIVE* full questions, selecting *ONE* full question from each unit.

UNIT - I

1 a.	a. Define Mechatronics and with a block diagram explain the engine manager	nent system.	10						
b.	b. Compare open loop and closed loop control system with a neat diagram	n, explain generalized	10						
	measurement system.		10						
2 a.	a. Define sensor. Explain principle and working of Eddy current proximi	ty sensor with a neat	10						
	sketch.		10						
b.	b. Explain principle and working of Hall-effect sensor with an example and no	eat sketch.	10						
	UNIT - II								
3 a.	a. Explain working principle of brushless permanent magnet DC motor with a	neat sketch.	10						
b.	b. Explain: i) Bipolar transistors ii) Solenoids.		10						
4 a.	a. Explain with a neat diagram single phase induction motors.		10						
b.	b. Explain principle and working of variable reluctance stepper motor with a r	neat sketch.	10						
	UNIT - III								
5 a.	a. Explain the process of converting ADC signals.		10						
b.	b. What is pulse modulation? Explain the two types of modulations.		10						
6 a.	a. What is data acquisition? Explain with a block diagram the DAQ system.		10						
b.	b. Explain different filters with frequency versus gain curves along with their	characteristic curve.	10						
	UNIT - IV								
7 a.	a. What is a microcontroller? Explain organization of microcontroller.		10						
b.	Explain High level, Assembly and Machine level language programming.								
8 a.	What are the types of registers used in 8085 microprocessor? Explain any three registers.								
b.	b. Draw a neat layout of architecture of INTEL 8085A microprocessor.		10						
	UNIT – V								
9 a.	a. What are logic gates? Explain AND, OR and NOT gates with symbols and	truth table.	10						
b.	With a proper example, explain how a negative number is represented in binary number system?								
10 a.	a. Write a circuit symbol and truth table for NAND, NOR and X-OR gates.		12						
b.	b. Convert;								
	i) 10011 to decimal ii) 4161 ₍₈₎ into binary		8						
	iii) F6D9 ₍₁₆₎ to decimal iv) 1011010111 ₍₂₎ to hexadecimal								